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# A High Frequency FM Receiver

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# A High Frequency FM Receiver



05/01/02

**Introduction**

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**Specifications**

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**Implementation**

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**Conclusion**

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**Appendix A: Block Diagram / Specifications**

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**Appendix B: Data Sheets**

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**Appendix C: Gantt Chart**

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**Appendix D: Proposal and Progress Reports**

Abstract.....	iii
Introduction:.....	1
Background Information:.....	1
Objective:.....	1
Specifications of the Receiver:.....	2
Implementation: .....	3
Identify project:.....	3
Research Theory:.....	3
Develop Schematic:.....	4
Assemble Receiver:.....	4
Tuner:.....	4
Mixer:.....	9
Low-Pass Filter:.....	10
IF Amplifiers:.....	11
PLL/Audio Detector:.....	12
Audio Amplifier and Speaker: .....	13
Test Receiver:.....	14
Conclusion:.....	15
Appendix A: Block Diagram / Schematics.....	16
Appendix B: Data Sheets.....	20
Appendix C: Gantt Chart.....	21
Appendix D: Proposal and Progress Reports.....	22

**Abstract**

The EET department at IPFW has requested the design of a senior project for successful completion of the EET degree. The author has designed and attempted to construct a High Frequency FM Receiver to demonstrate the concepts and skills necessary for the EET degree such as: theory of design, breadboarding, component-level testing and troubleshooting, and final circuit construction. The report describes the tuner, local oscillator, mixer, IF amplifier, PLL audio detector, and audio output sections that were devised to receive the 40-50 MHz FM radio band.