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# A Microprocessor Controlled Scoreboard

Robert W. Gibb III

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SENIOR DESIGN PROJECT

MICROPROCESSOR CONTROLLED SCOREBOARD

BY

ROBERT W. GIBB III

APRIL, 1986

ABSTRACT  
OF  
MICROPROCESSOR CONTROLLED SCOREBOARD

BY

ROBERT W. GIBB III

Due to the increased development of microprocessors, the expansion of its use seems only logical. A microprocessor controlled scoreboard would be easier to design, construct, and troubleshoot than a hardware based design. A 4\*3 matrix keyboard with seven-segment display outputs provides the user with simple and versatile control. System software allows flexibility in operation and hardware expansion.

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